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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/617,454	07/10/2003	Anssi Haverinen	878.0034.U1(US)	3229

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EXAMINER

PATEL, NIKETA I

ART UNIT PAPER NUMBER

2182

DATE MAILED: 09/27/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/617,454	Applicant(s) HAVERINEN ET AL.	
	Examiner Niketa I. Patel	Art Unit 2182	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 7/10/2003.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-14 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-14 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 12 January 2002 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date <u>1/26/2005</u> | 6) <input type="checkbox"/> Other: _____ |

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DETAILED ACTION

Claim Rejections - 35 USC § 112

1. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter, which the applicant regards as his invention.

2. Claims 1-14 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

- a. Claims 1 and 13 recites, “**the device**” through out the claim language to refer to several different types of devices. For example, Claim 1, line 3 recites “the device having” and Claim 13 line 6, recites “operation of the device,” it’s unclear whether the applicant is referring to the **data handling device** or the **two or more devices**.

- b. Claim 1, recites “**first mode**” and “**second mode**,” it is unclear as to whether these modes are for the data handling device or the identity acquisition unit.

Claim Objections

3. Claims 2, 5 is objected to because of the following informalities:

- c. Claim 2, line 2 recites a phrase which appears to be missing few words “**to process the or each data word**”. Appropriate correction is required.

- d. Claim 5, line 3 recites a phrase which appears to be missing few words “**bits of the or each said data word**”. Appropriate correction is required.

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. As far as the examiner can interpret the claims in light of the 35 U.S.C. 112, second paragraph, rejection, supra, the claims 1-14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Horng et al. U.S. Patent Number: 6,738,788 B1 (hereinafter "*Horng*") and further in view of Dabral et al. U.S. Patent Number: 6,192,431 B1 (hereinafter "*Dabral*").

6. Referring to claims 1, 13-14, *Horng* teaches a method and a data handling device [see column, lines and figure 1, element 14] capable of operating in a system in which two or more devices [see column 1, lines 18-22, 'ICs' and figure 1, element 10] are connected by a data bus for the transmission of communications therebetween, an identity acquisition unit [see column 4, lines 52-62 and figure 1, element 12] capable of functioning in a first mode of operation of the device to receive data transmitted over the data bus and in response to the order in which the bits of one or more data words of a predetermined form are received on the data bus connectors during the first mode of operation determine an identity for the device and store the identity in an identity store of the device [see column 4, lines 34-62], and a data handling unit capable of functioning in a second mode of operation of the device to handle communications transmitted over the bus and that specify the identity stored in the data store as a destination [see column 4, lines 63-65 and column 5, lines 27-36 and figure 1, elements 14, 20.] *Horng* is silent regarding the data bus having two or more data lines and the device having: two or more data bus

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connectors, each for connection to a respective data line of the data bus [see column, lines]

however, *Dabral* teaches these limitations [see *Dabral* column 1, lines 33-37.]

One of ordinary skill in the art at the time of applicant's invention would have clearly recognized that it is quite advantageous for the integrated circuit of *Hornig* to have two or more data lines connected to respective connectors in order to allow the IC to communicate with other elements. It is for this reason that one of ordinary skill in the art would have been motivated to use IC with two or more data lines connected to respective connectors in order to allow the IC to communicate with other elements.

7. **Referring to claim 2**, teachings of *Hornig* as modified by the teachings *Dabral* teaches wherein the identity acquisition unit is arranged to process the or each data word of a predetermined form in accordance with a look-up table in order to determine the identity for the device [see *Hornig* column 4, lines 34-62, 'database'.]

8. **Referring to claim 3**, teachings of *Hornig* as modified by the teachings *Dabral* teaches comprising a multiplexing arrangement located between the data bus connectors and the data handling unit and arranged to, in at least the second mode of operation, re-order in accordance with the stored identity data received from at least two of the data lines of the bus and passed to the data handling unit [see *Dabral* figure 1a, elements 112, 'multiplexers,' 121, 111, 120, 110.]

9. **Referring to claim 4**, teachings of *Hornig* as modified by the teachings *Dabral* teaches wherein the multiplexing arrangement is a hardware multiplexing arrangement [see *Dabral* figure 1a, element 112, 'multiplexers'.]

10. **Referring to claim 5**, teachings of *Hornig* as modified by the teachings *Dabral* teaches wherein the identity acquisition unit is arranged to determine the identity in accordance with a

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deviation in the order of at least some of the bits of the or each said data word from a standard order, and the multiplexing arrangement is arranged to re-order the data lines of the bus so as to restore the standard order to the bits as applied to the data handling unit.

11. **Referring to claim 7**, teachings of *Hornig* as modified by the teachings *Dabral* teaches wherein the device is a memory device [see *Hornig* column 1, lines 18-22, 'ICs' and figure 1, element 10.]

12. **Referring to claim 8**, teachings of *Hornig* as modified by the teachings *Dabral* teaches wherein the device is defined on an integrated circuit and the data bus connectors are connectors for communicating to and/or from the integrated circuit [see *Hornig* column 1, lines 18-22, 'ICs' and figure 1, element 10 and *Dabral* figure 1a, elements 112, 'multiplexers,' 121, 111, 120, 110.]

13. **Referring to claim 9**, teachings of *Hornig* as modified by the teachings *Dabral* teaches the data handling system comprising two or more data handling devices, interconnected by the said data bus [see *Hornig* column 1, lines 18-22, 'ICs' and figure 1, element 10.]

14. **Referring to claim 10**, teachings of *Hornig* as modified by the teachings *Dabral* teaches comprising a further device connected to the bus and capable of functioning to transmit the said one or more data words of a predetermined form over the data bus [see *Dabral* column 1, lines 33-43 and figure 1a, elements 112, 'multiplexers,' 121, 111, 120, 110.]

15. **Referring to claim 11**, teachings of *Hornig* as modified by the teachings *Dabral* teaches wherein the further device is capable of triggering the data handling devices to enter the first mode of operation [see column 4, lines 34-62 and column 1, lines 14-37.]

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16. Referring to claim 12, teachings of *Horn* as modified by the teachings *Dabral* teaches wherein the data handling devices are arranged to automatically enter the first mode of operation an initialization of the system [see column 4, lines 34-62 and column 1, lines 14-37.]

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The following documents have been made record of to further show the state of the art as it pertains to ID generation unit:

Farmwald et al. U.S. Patent number 6,415,339 B1

Lofstrom U.S. Patent number 6,161,213

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Niketa I. Patel whose telephone number is (571) 272 4156. The examiner can normally be reached on M-F 8:00 A.M. to 5:00 P.M.

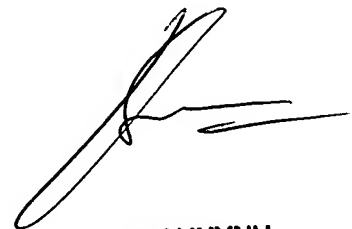
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dov Popovici can be reached on (571) 272 4083. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

NP

09/20/2005



KIM HUYNH
PRIMARY EXAMINER

9/22/05